

Xingwei Wang

ELECTRONICS I/II LAB

WHO I AM?

✘ Education

- + MS, Ph.D., Electrical Engineering Virginia Tech
- + Certificate, Future Professoriate Virginia Tech

✘ <http://faculty.uml.edu/xwang/>

✘ Contact

- + Office Hours: Tuesdays 1 pm – 3 pm;
or, make an appointment
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SYLLABUS

- ✘ <http://faculty.uml.edu/xwang/16.311/syllabus.htm>
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- ✘ Quiz after each lecture session
- ✘ 7 Experiments
- ✘ Final Report

GRADING POLICY

Quiz after each lecture session	10%
Experiment (7 x 10 points)	70%
Final Report	20%
Total	100%

EXPERIMENT GRADING POLICY (IF NOT SPECIFIED)

Quiz before the experiment (Cannot perform the experiment without TA's approval. TA will check your notebook. You should have written clearly the objectives, design (including the parameters of the components), and experiment methods in your notebook.	3 points
Notebook record (accuracy; clarity; neat) results & discussion; Bench tests (TA will ask questions related to the experiment and equipment usage)	6 points
Clean up the lab space	1 point
Total	10 points

FINAL REPORT GRADING

Background/Problem	5 points
Approach/Method	5 points
Results	5 point
Discussion/Analysis	5 points
Total	20 points

CLASS SCHEDULE

- ✘ Tentative. Please check frequently for updates.
- ✘ Lecture topics do NOT necessarily follow the order of the schedule.
- ✘ Topic contents may need more or less time than what is allocated.